



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

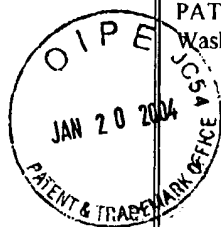
Electronic Version v18

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Title of Invention	Method for Enhancing Electrode Surface Area in DRAM Cell Capacitors																																																
<p>Application Number: 10/050390</p> <p>Confirmation Number: 6193</p> <p>First Named Applicant: Donald Yates</p> <p>Attorney Docket Number: MTI-31269</p> <p>Art Unit: 2811</p> <p>Examiner: Cuong Quang Nguyen</p> <p>Search string: (5937294 or 6188097 or 5612560 or 20010001210).pn.</p> <p>RECEIVED JAN 21 2004 TECHNOLOGY CENTER 2800</p> <p>Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:</p> <p>That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>ca</td><td>1</td><td>5937294</td><td>1999-08-10</td><td>Sandhu et al.</td><td></td><td>438</td><td>255</td></tr><tr><td>ca</td><td>2</td><td>6188097</td><td>2001-02-13</td><td>Derderian et al.</td><td>B1</td><td>257</td><td>306</td></tr><tr><td>ca</td><td>3</td><td>5612560</td><td>1997-03-18</td><td>Chivukula et al.</td><td></td><td>257</td><td>309</td></tr></tbody></table> <p>US Published Applications</p> <p>Note: Applicant is not required to submit a paper copy of cited US Published Applications</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Pub. No.</th><th>Date</th><th>Applicant</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>ca</td><td>1</td><td>20010001210</td><td>2001-05-17</td><td>Rhodes et al.</td><td>A1</td><td>257</td><td>296</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	ca	1	5937294	1999-08-10	Sandhu et al.		438	255	ca	2	6188097	2001-02-13	Derderian et al.	B1	257	306	ca	3	5612560	1997-03-18	Chivukula et al.		257	309	init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass	ca	1	20010001210	2001-05-17	Rhodes et al.	A1	257	296
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Signature

Examiner Name	Date
<i>Qgby</i>	04-27-04



U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE
Washington, D.C. 20231

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

ATTY. DOCKET NO.

MTI-31269

Applicant

Yates, Donald L. et al.

Filing Date

January 16, 2002

Serial No. *

10/050,390

Confirmation No.

6193

Group Art Unit

2811

U.S. PATENT DOCUMENTS

		Patent Number	Issue Date	Patentee	U.S. Class	Sub-Class	Filing Date
	A1						

FOREIGN PATENT OR PUBLISHED FOREIGN PATENT APPLICATION

Examiner Initial		Document Number	Publication Date	Country	Int'l Class	Sub-Class	Translation (Yes/No)
<i>cn</i>	B1	0 567 748 A1	03-11-93	EPO	G11B	5/64	N/A

OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

Examiner Initial		Non-Patent Document
	C1	

Examiner Initials	<i>296</i>	Date Considered	<i>04-27-04</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			